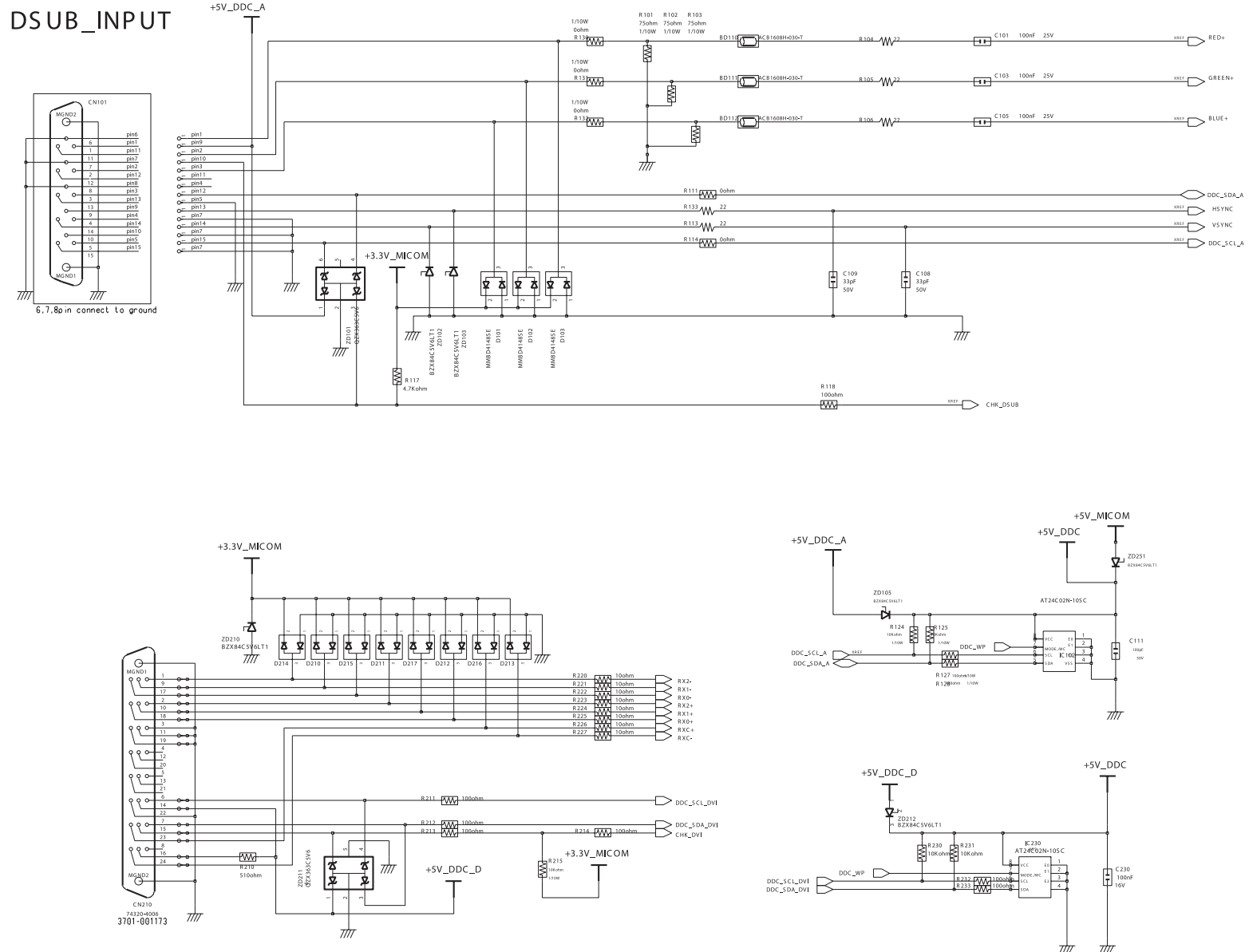


## 9 Schematic Diagrams

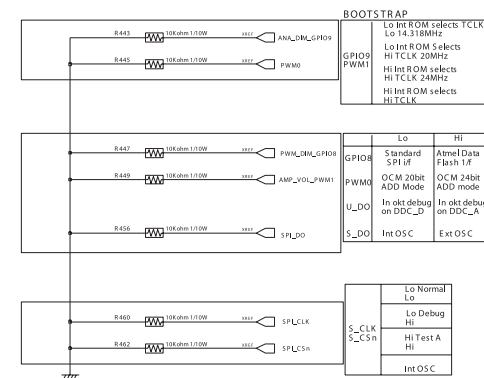
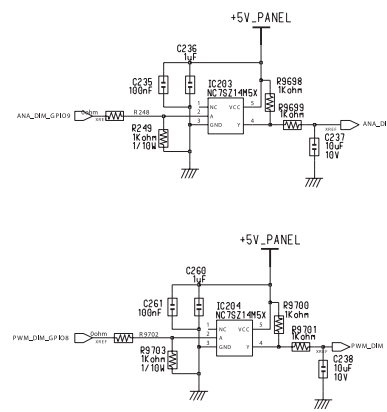
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### 9-1 Schematic Diagrams

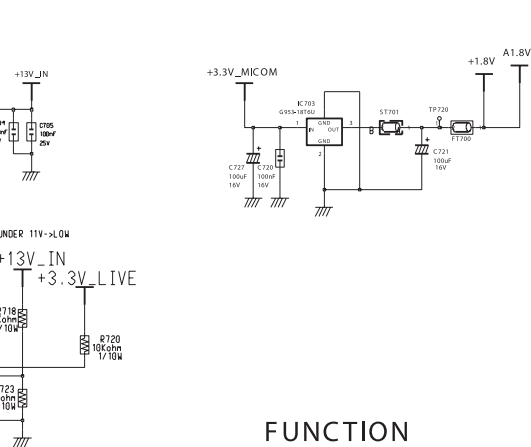
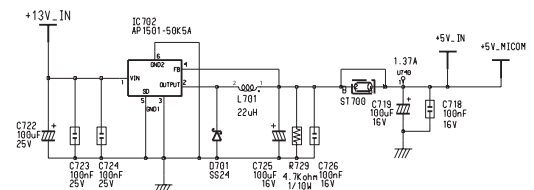
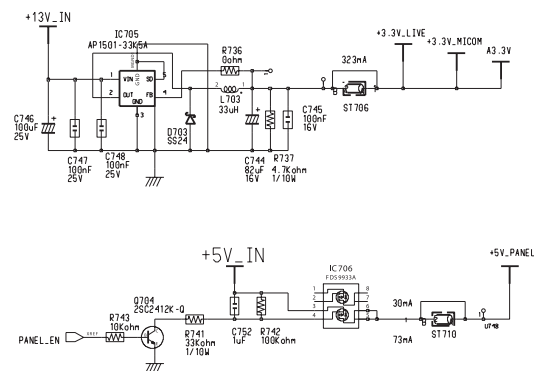
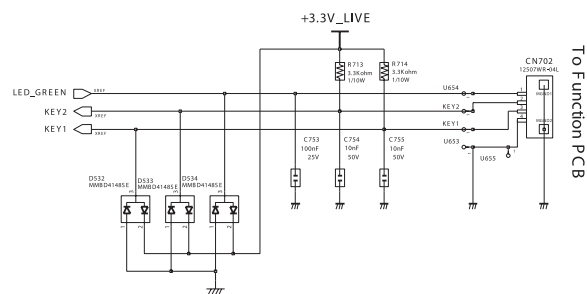


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The schematic diagram illustrates the IP-BOARD circuit. It features a central IP board with pins labeled C/N700, U700, P701, U702, U725, U704, U727, U728, U739, and U710. The board is connected to a +3.3V\_LIVE supply through a network of resistors (R700, R712, R716, R722) and capacitors (C700, C709). A +13V\_IN supply is connected to the IC701 (LM1035) and the BL\_EN line. The BL\_EN line is also connected to the IP board via a resistor (R700) and a capacitor (C709). The IP board is connected to a PWM\_DIM signal through a resistor (R700) and a capacitor (C709). The IP board is also connected to an ANA\_DIM signal through a resistor (R711) and a capacitor (C709). The IP board is connected to a ground plane through a resistor (R712) and a capacitor (C709).

[illegible]

## Memo